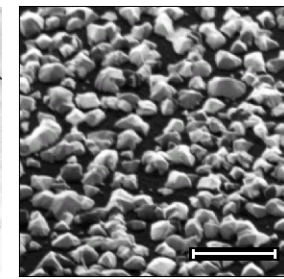
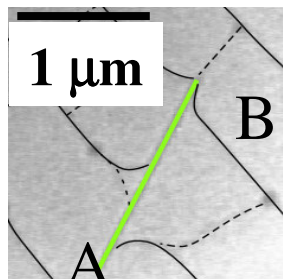
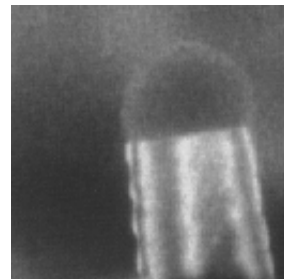
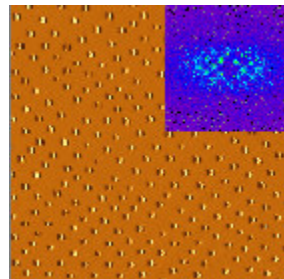
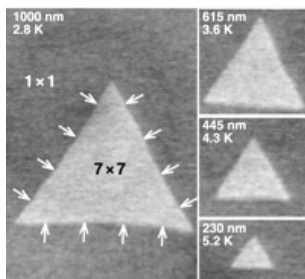


# Novel Materials for Organic and Thin Film Electronics

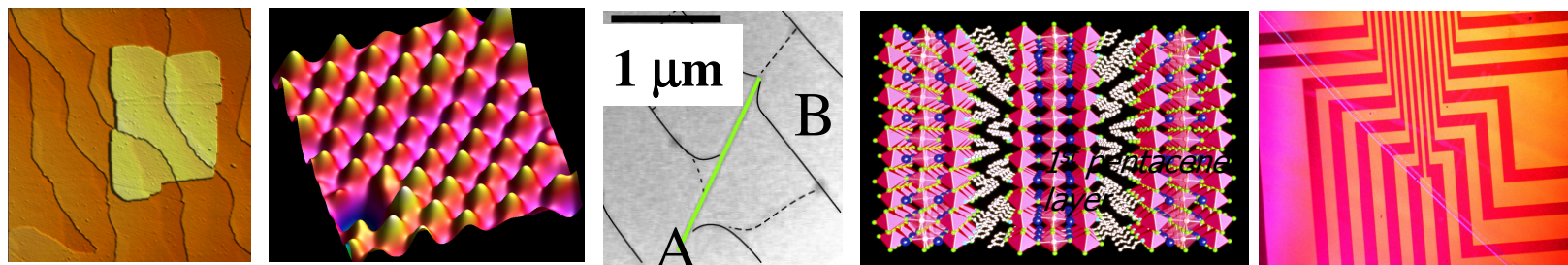


# The road beyond CMOS : Nano ?

Nano: things smaller than 100 nm

Opportunities for revolutionary new materials, processes, and technologies

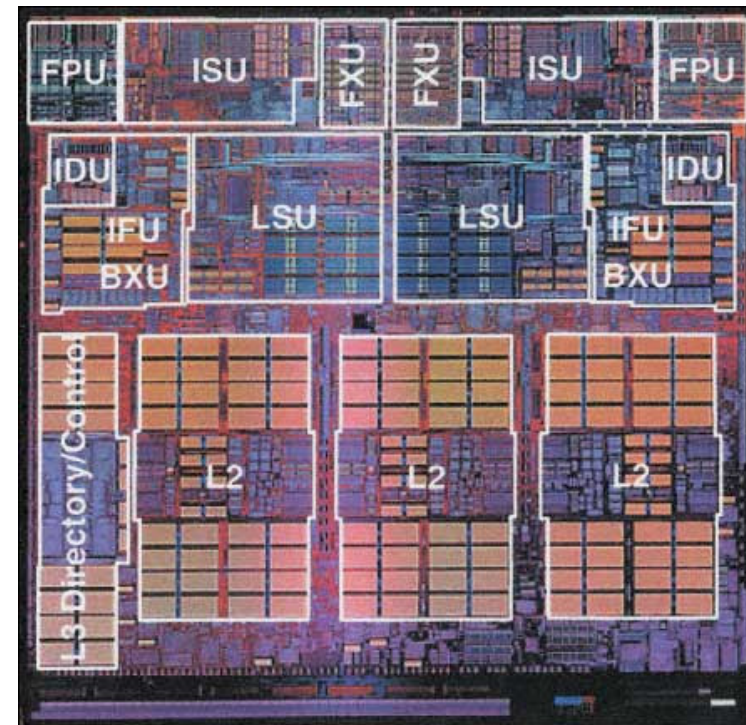
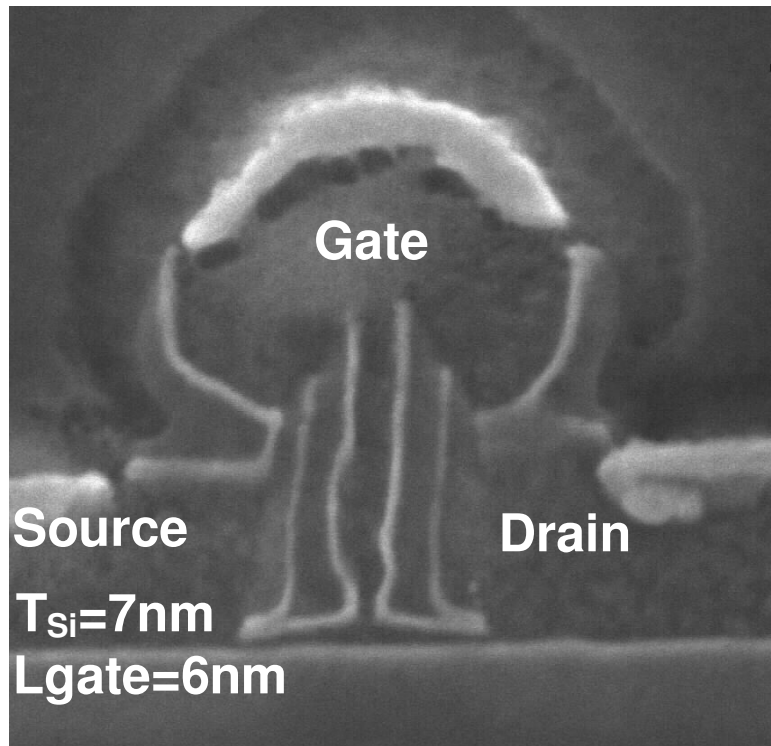
But: also many opportunities to improve, extend, and transform present technologies, including computer hardware technologies





# Silicon Logic - Already at the Nanoscale

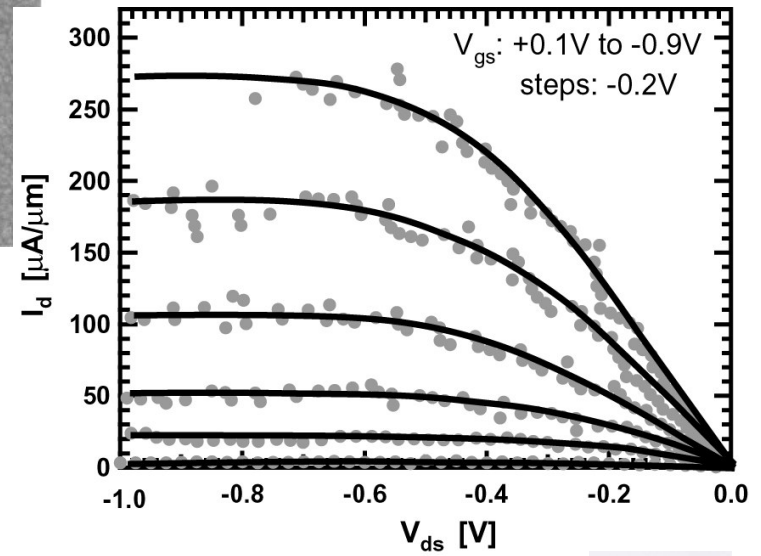
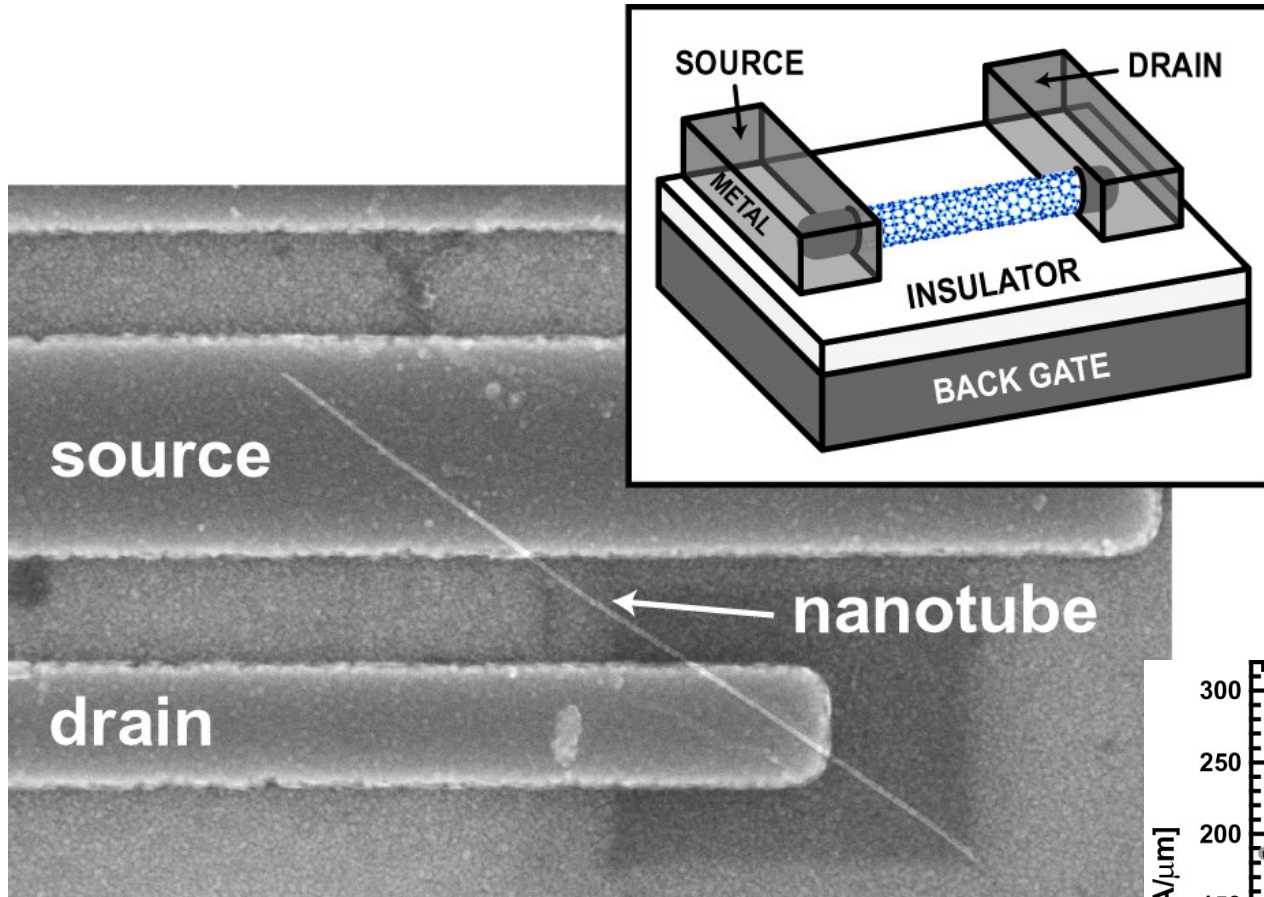
(That's what got us into trouble!)



- Gate length = 6 nm

- Power4 Chip
- 174 million transistors

# One alternative: Carbon Nanotube FET

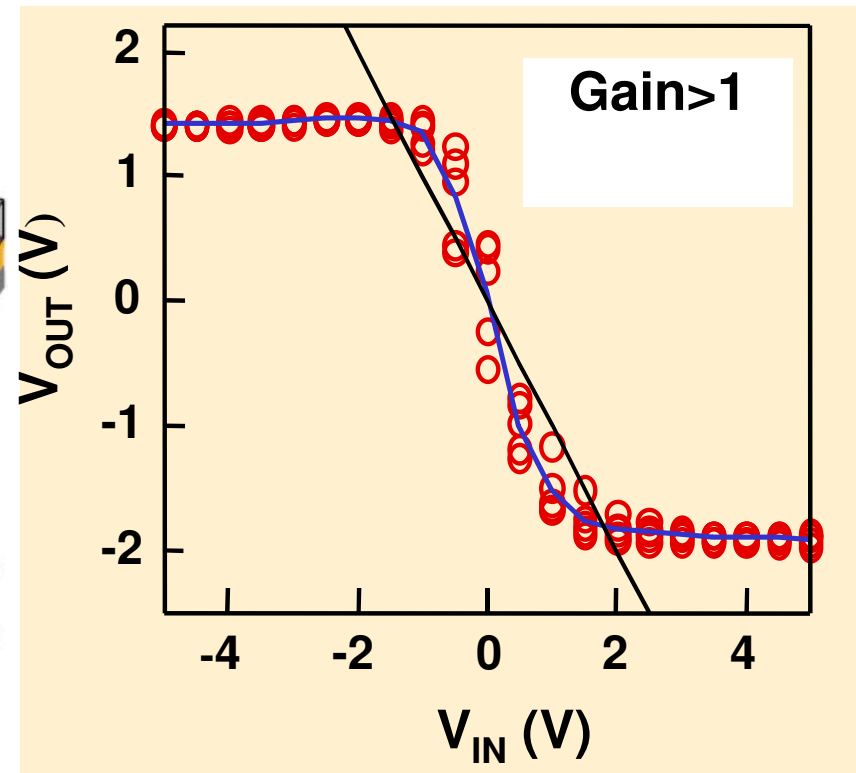
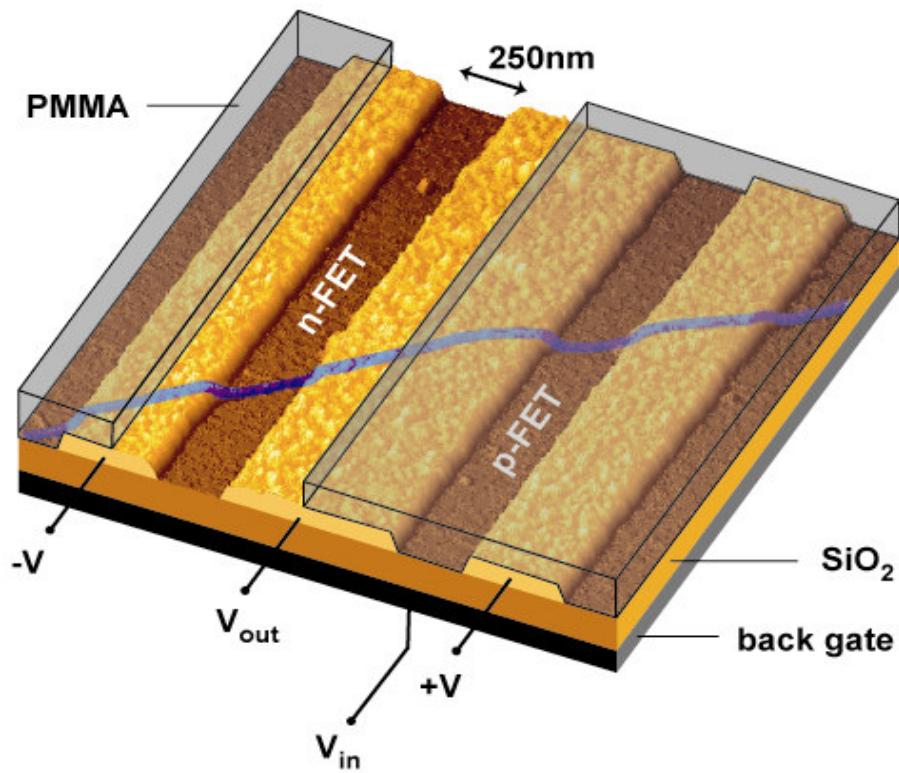


## Semiconductor mobilities @ RT (cm<sup>2</sup>/V.s)

InSb	77,000
CdSe	650
c-Si	1,500 – 100
a-Si	1
CNT	100,000
Polymer	10 <sup>-5</sup> – 10 <sup>-2</sup>
Pentacene	0.1 – 5
Chalcogenides (spinc.)	1 -20



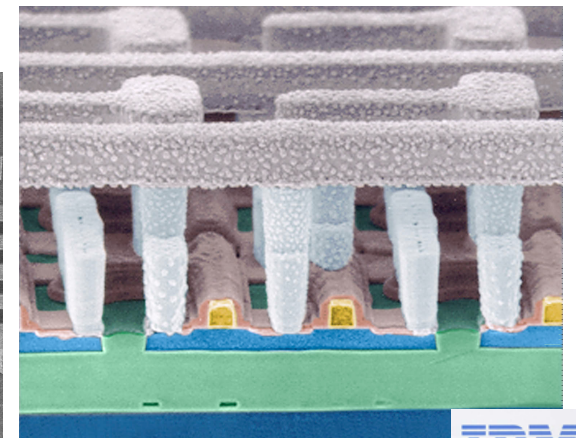
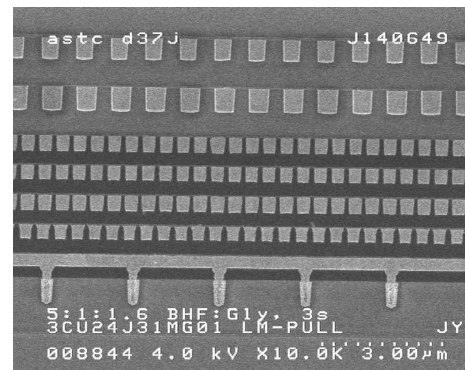
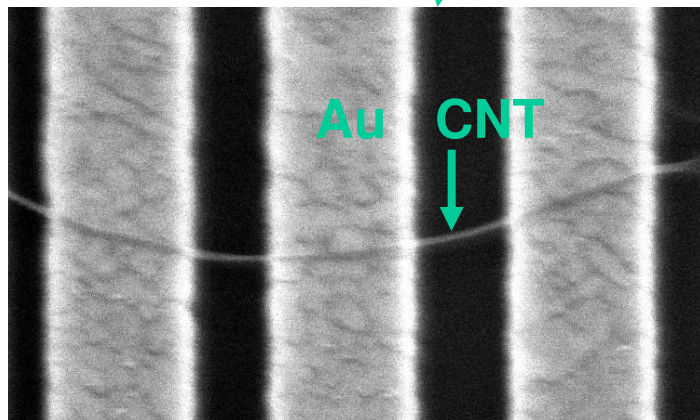
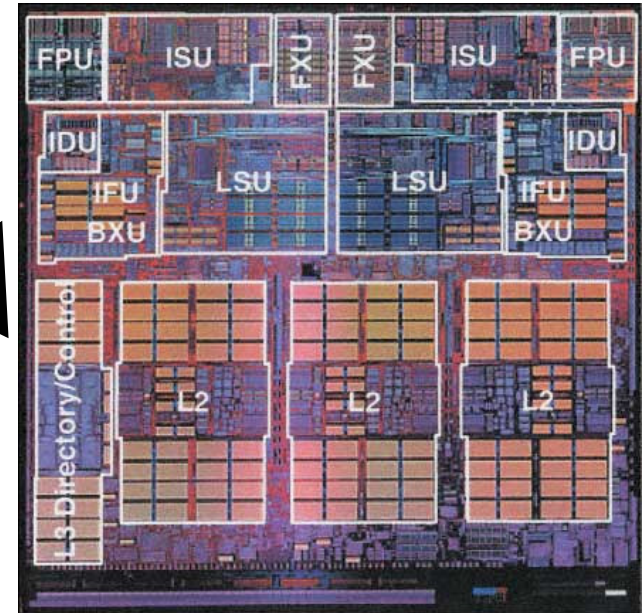
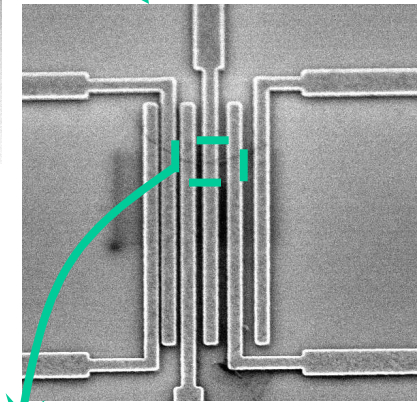
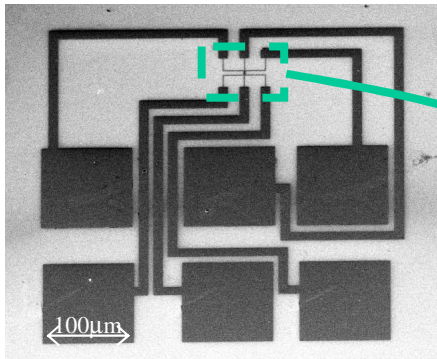
# Carbon Nanotube Inverter



# Nanotube Technology ?

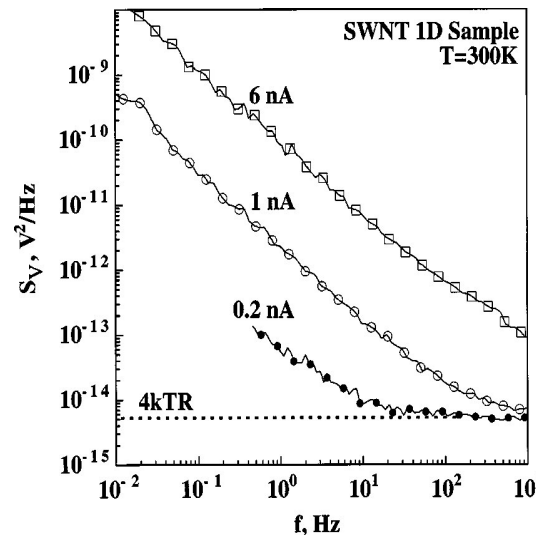
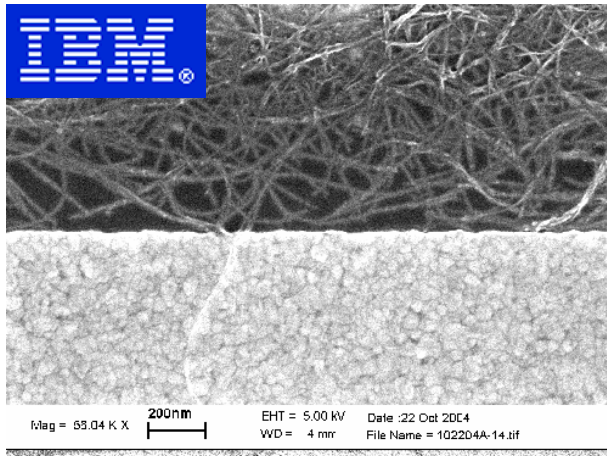
Plenty of room for improvement !  
No new architecture !

## How do you get from here to there?



# Some of the issues

- CNT synthesis and purification; control of diameter and chirality
- CNT placement in integrated circuit hierarchy with nm precision
- Site- and/or area-selective n- and p-type CNT doping on nm scale
- Control over contacts to CNT, injection barrier to n-, p-, i-CNTs
- Elimination of parasitics for high performance
- Elimination of 1/f noise in CNFET devices
- Theoretical modeling of CNT physics, chemistry, and devices
- CNT and device physical characterization
- Optoelectronic properties

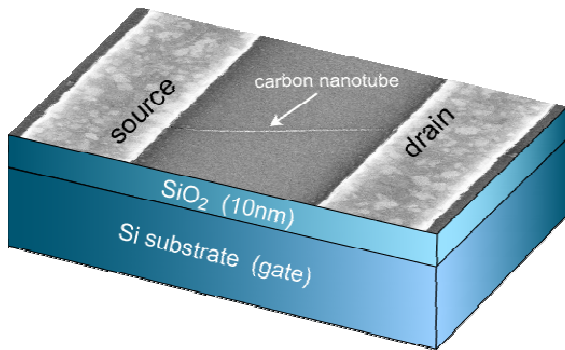
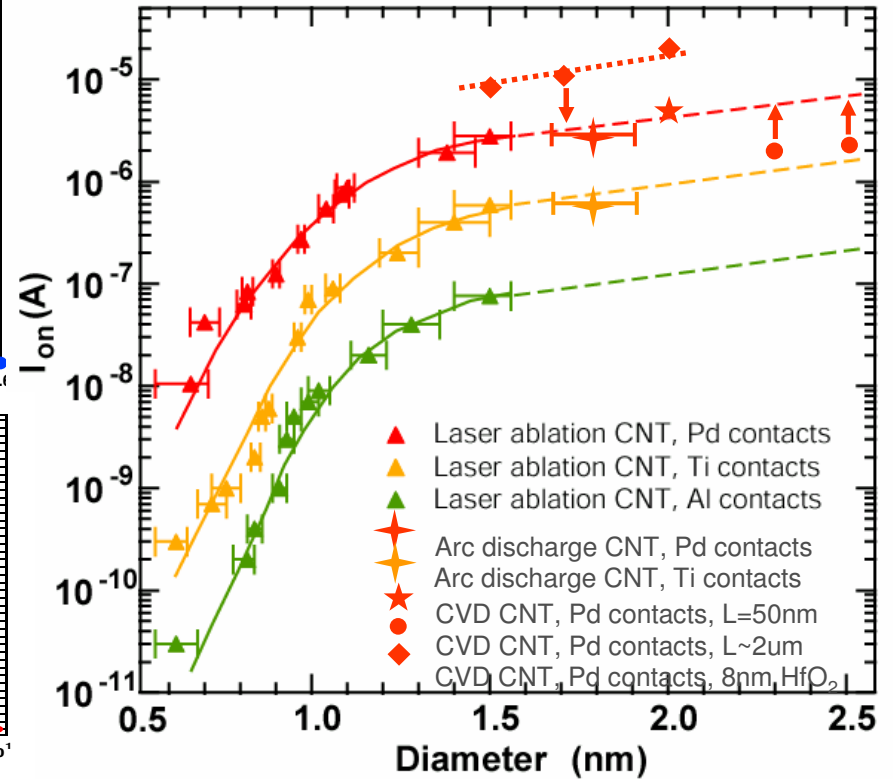
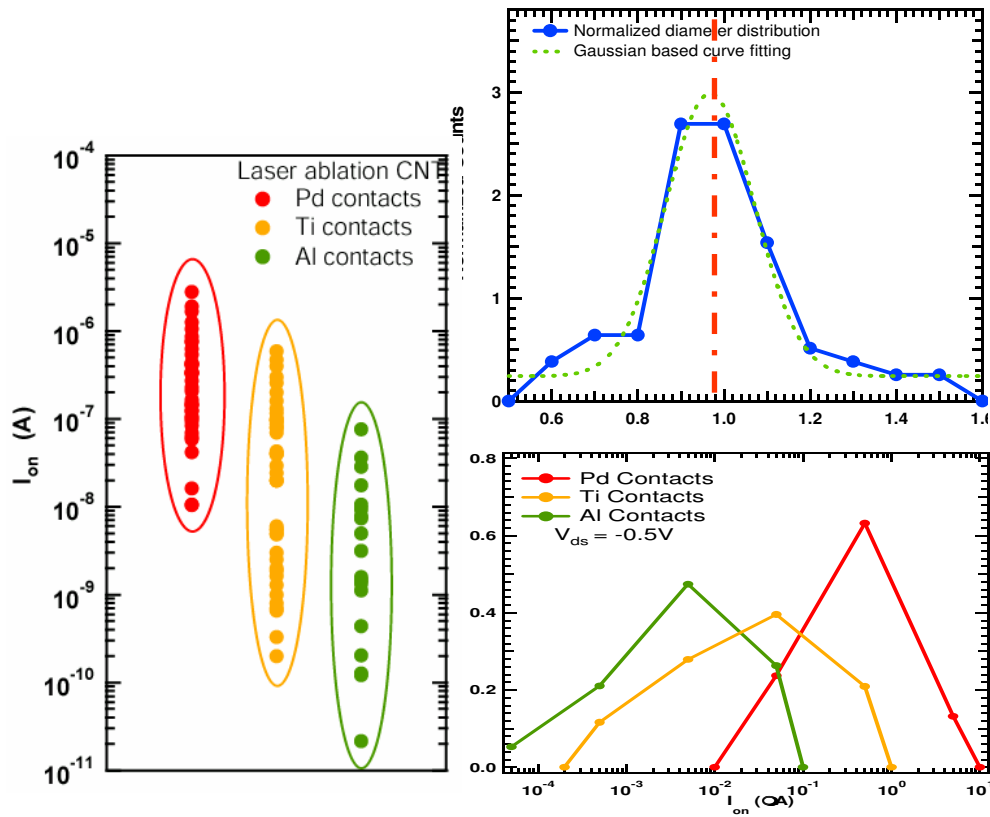


## 1/f noise in carbon nanotubes

Philip G. Collins, M. S. Fuhrer, and A. Zettl  
APL 76, 894 (2000)

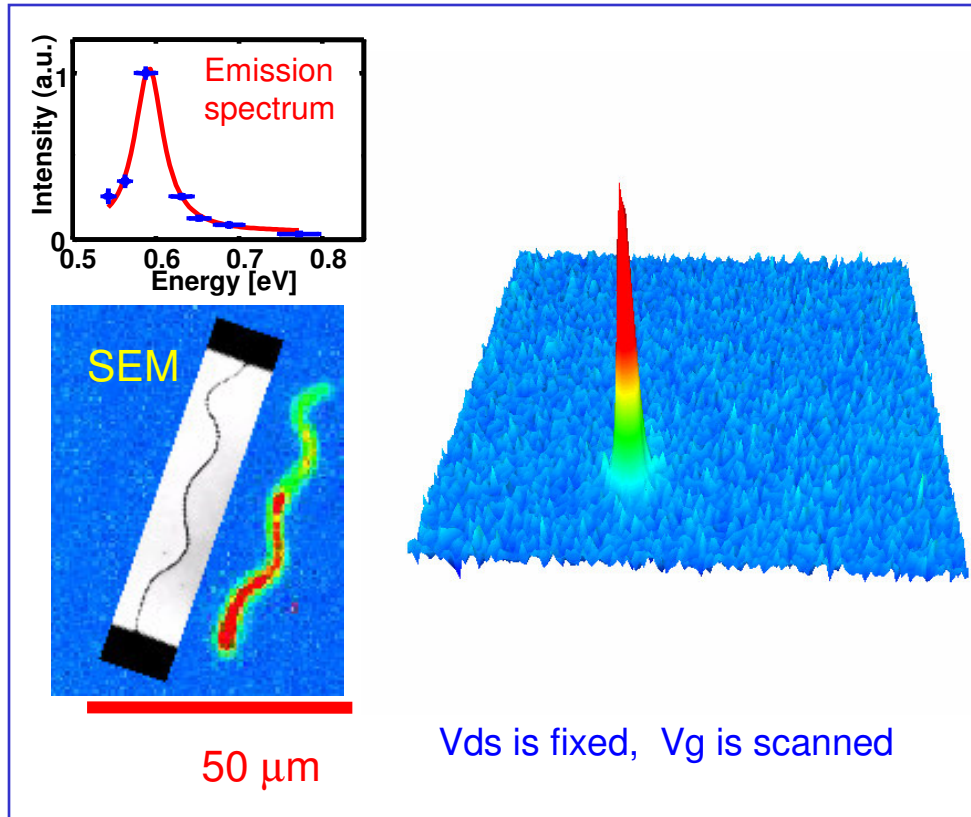


# Contacts



Zhihong Chen, Joerg Appenzeller, Joachim Knoch,  
 Yu-Ming Lin, Phaedon Avouris  
 A26-5

# CNT Optoelectronics

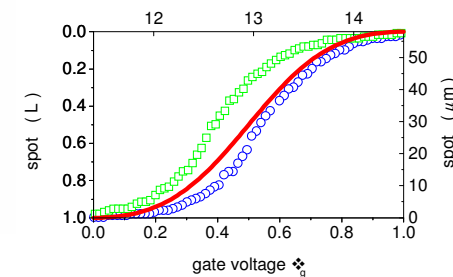
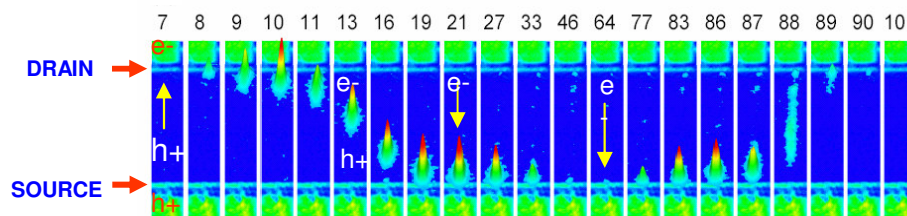


CNT optoelectronic properties are key in understanding basic transport properties, effects of defects, CNT electronic structure, excited states, etcetera.

There is room for much experimental work, as well as more advanced theoretical understanding.

Exciton binding energy is CNTs (0.1-0.5 eV) much larger than in III-V's (10's of meV). CNT exciton bonding energy depends on environment.

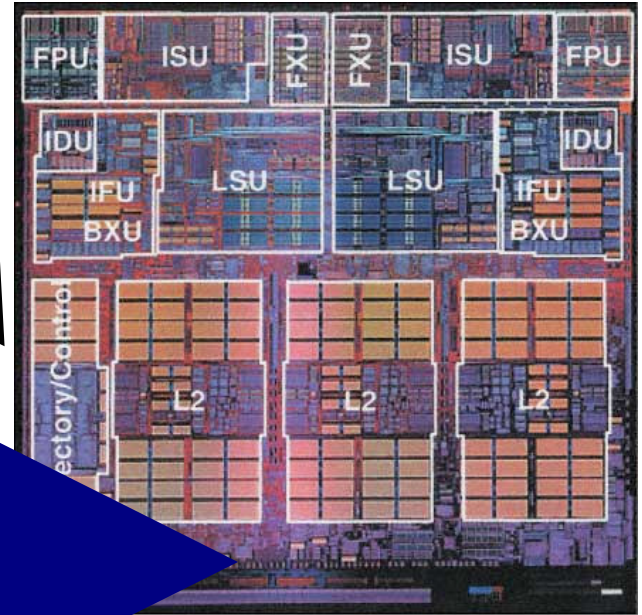
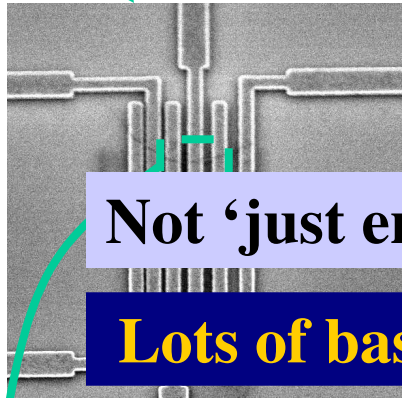
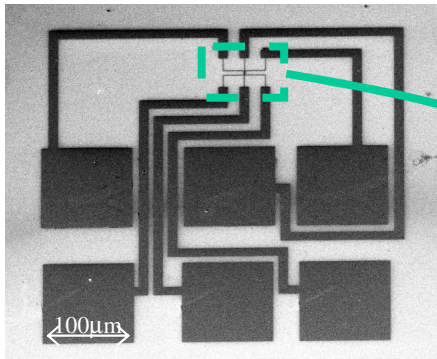
Ph. Avouris, A26-4



# Nanotube Technology ?

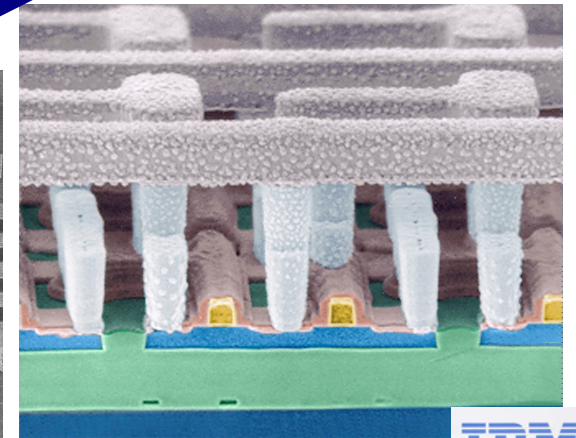
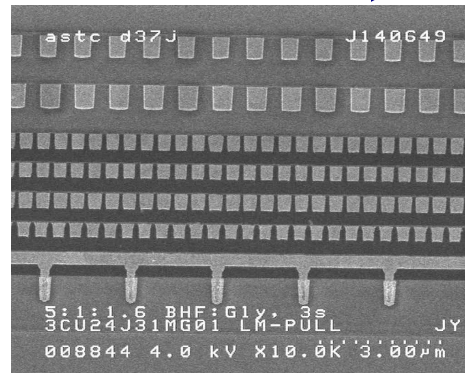
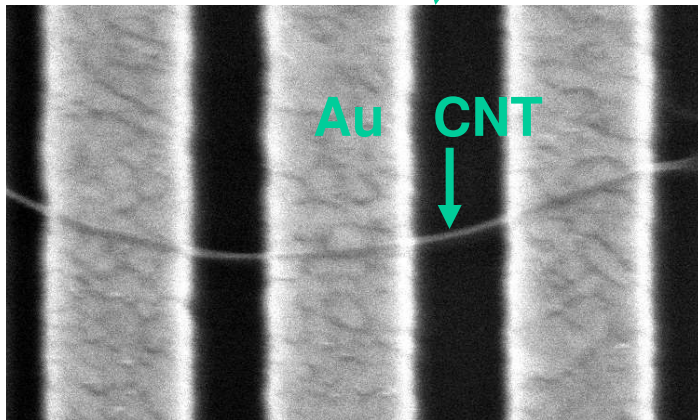
Plenty of room for improvement !  
No new architecture !

How do you get from here to there?



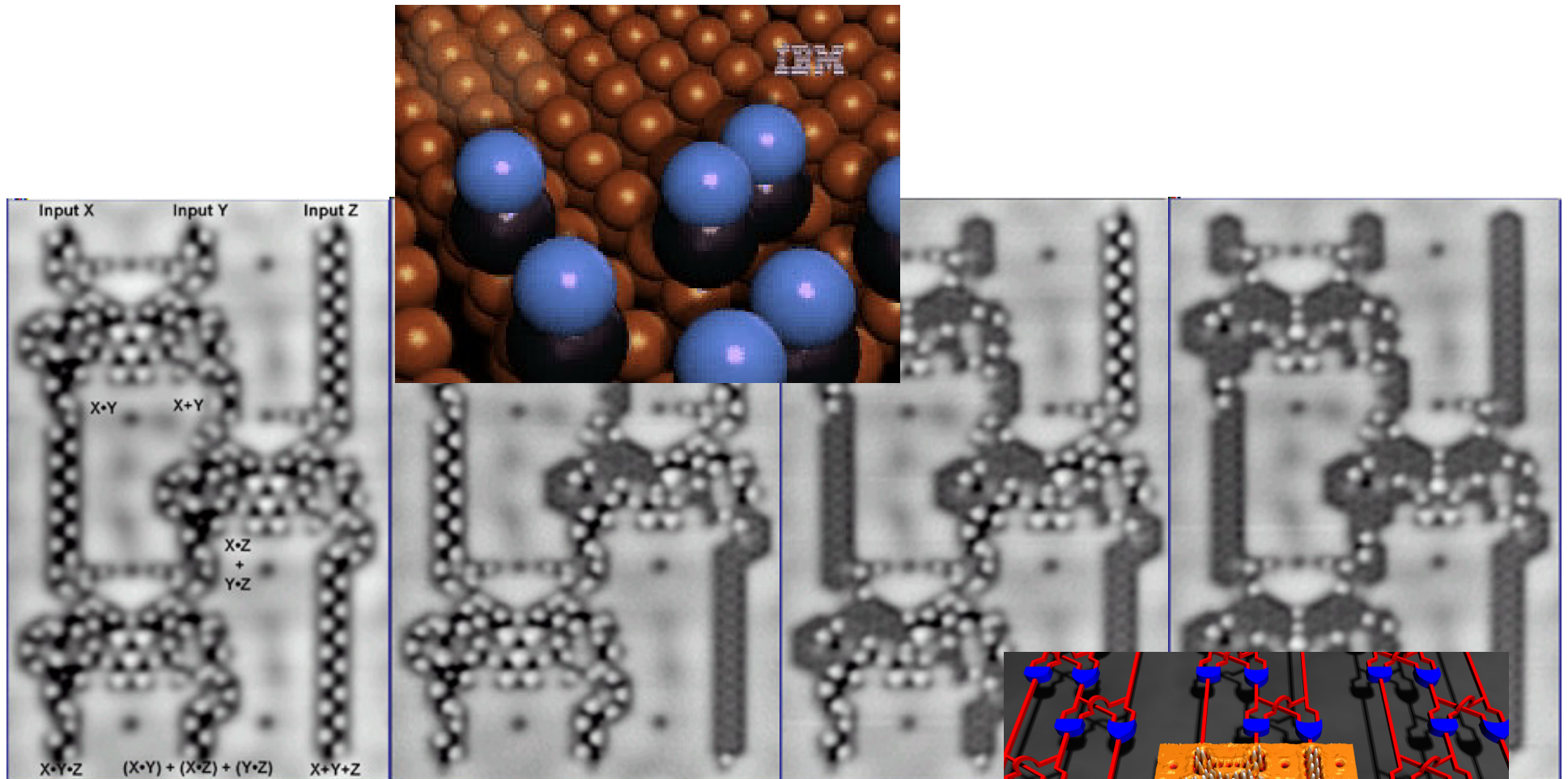
Not 'just engineering'

Lots of basic science !





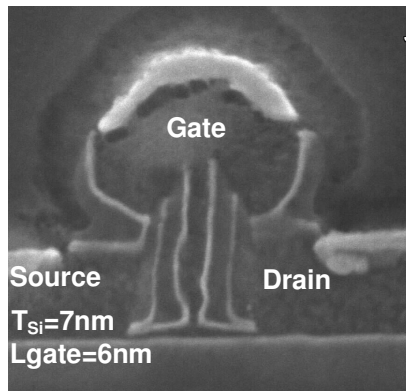
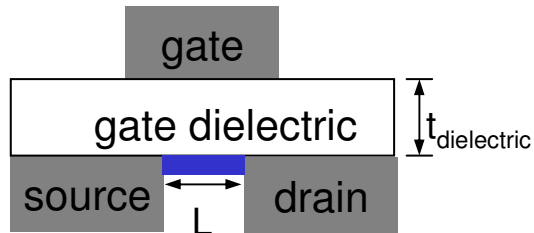
# A Molecular Computer – Slow but it works (once)



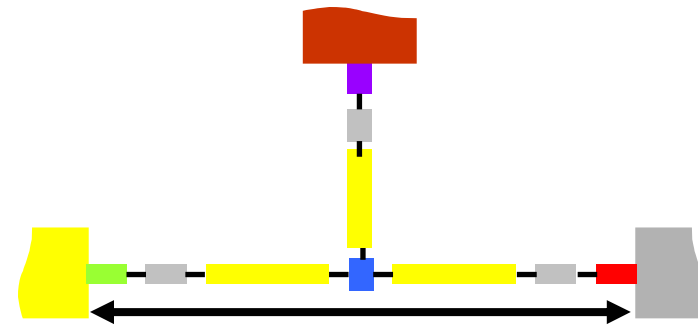
A.J. Heinrich, C.P. Lutz, J.A. Gupta, D.M. Eigler  
Science 2002

# Molecular Transistors

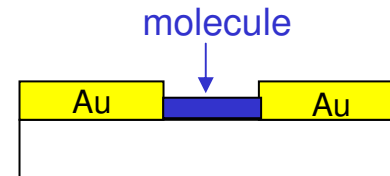
## Electrostatics of Molecular Transistors



Is Si really that much bigger?

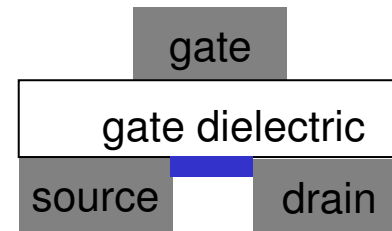


### 1 OFF state: Limited by Tunneling



Barrier lowered by  
Energy level offset  
Hybridization  
and Charge transfer  
between metal-molecule

### 2 Gate Modulation: Electrostatics

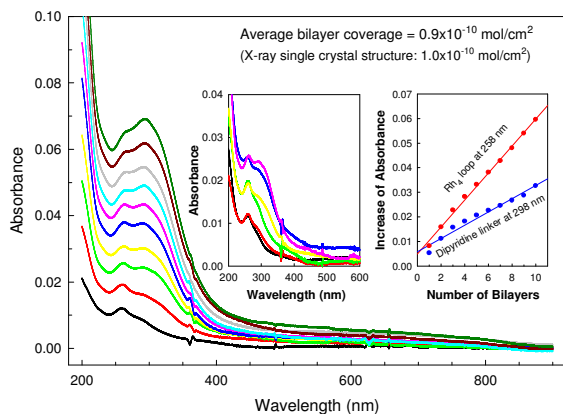
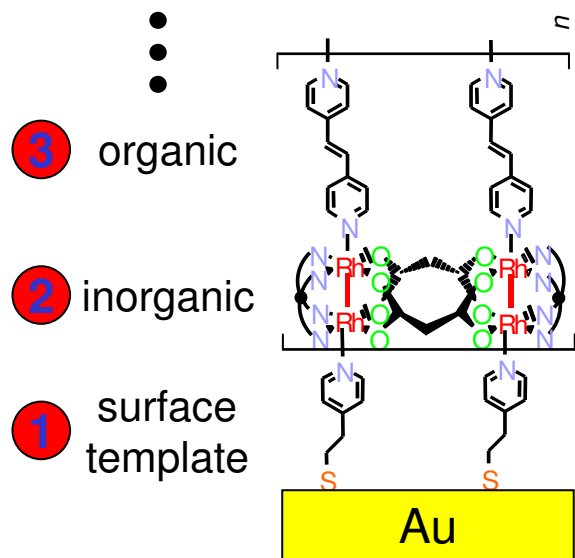


$t_{\text{dielectric}} \lesssim L$   
to attain gate field  
not dominated by  
drain field  
Yet  
dielectric not leaky

Design molecules with  $L > 2.5\text{-}3 \text{ nm}$   
Tailor tunnel barriers through chemistry

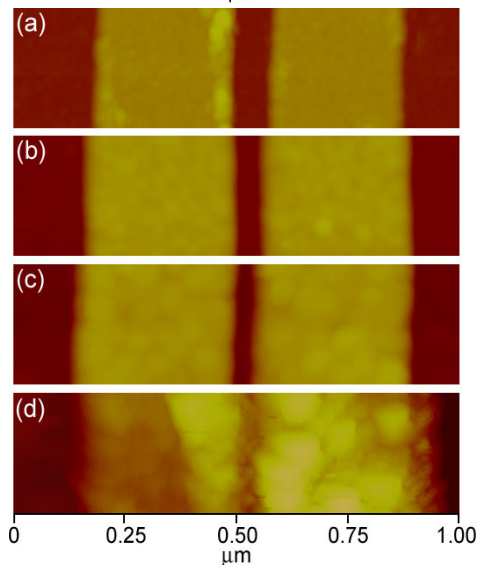
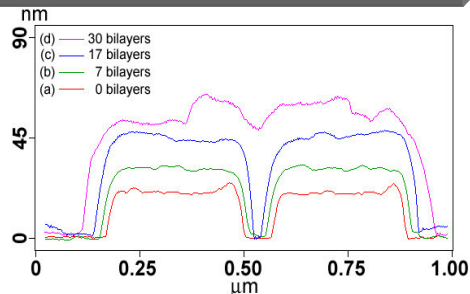
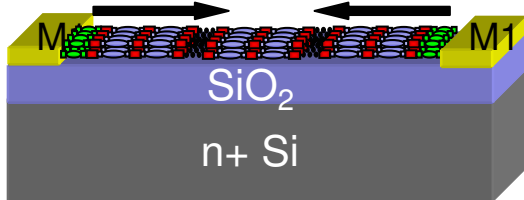
# Directed Assembly of Molecular Devices

## Layer-by-Layer Assembly



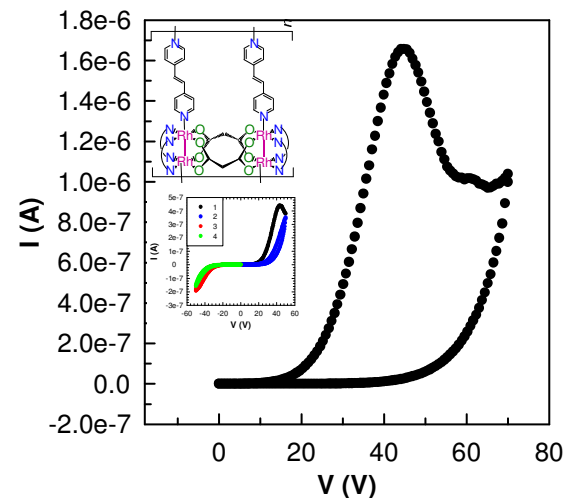
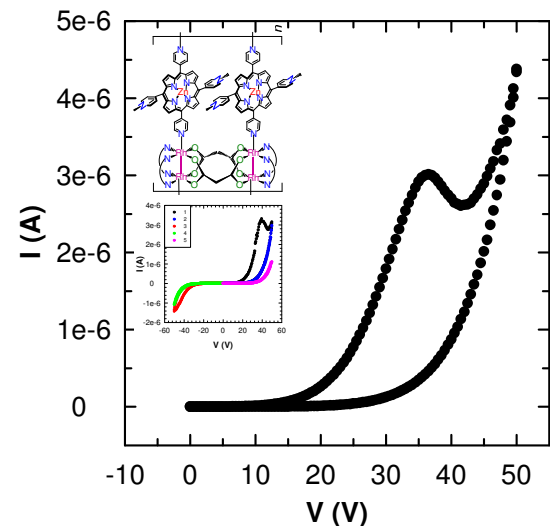
Manipulate chemistry of template, organic, and inorganic

## Device Assembly



Manipulate device geometry and molecular assembly

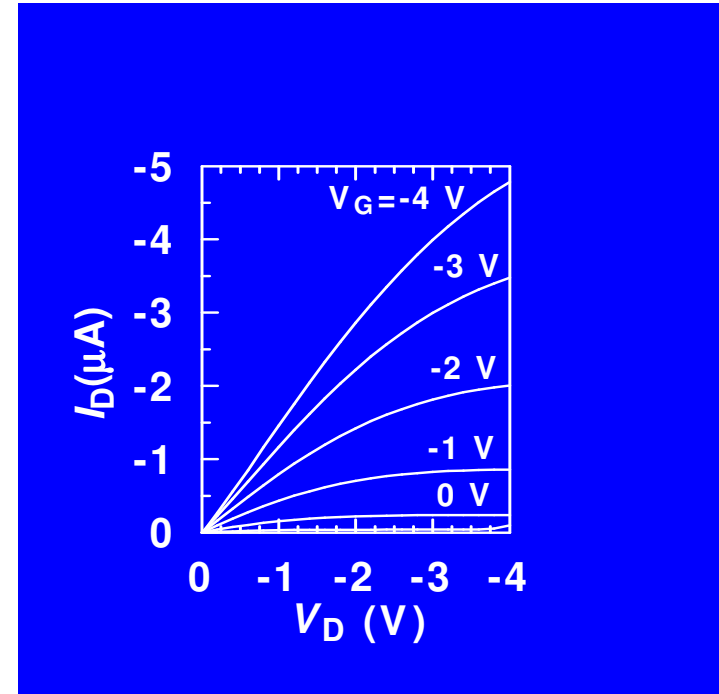
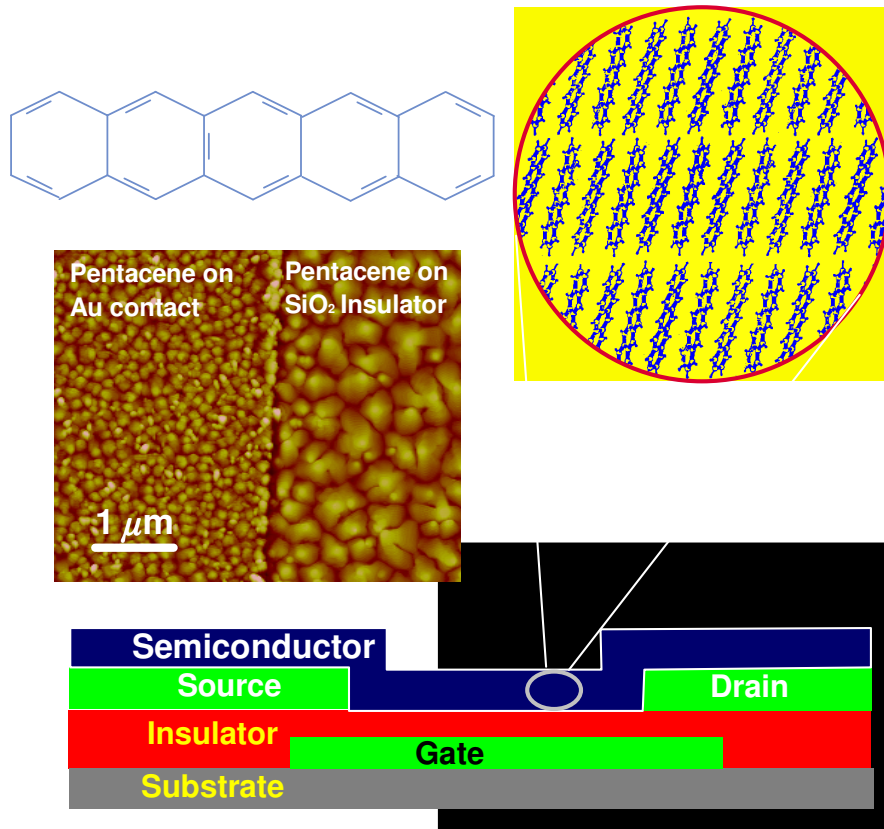
## Device Characteristics



Study low and high bias characteristics

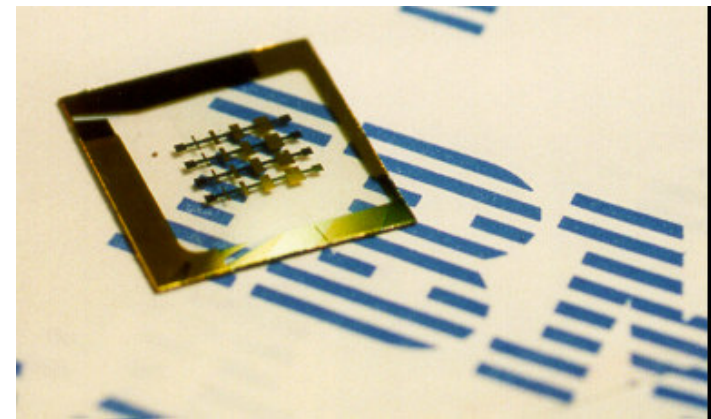


# Pentacene: the world's best organic semiconductor

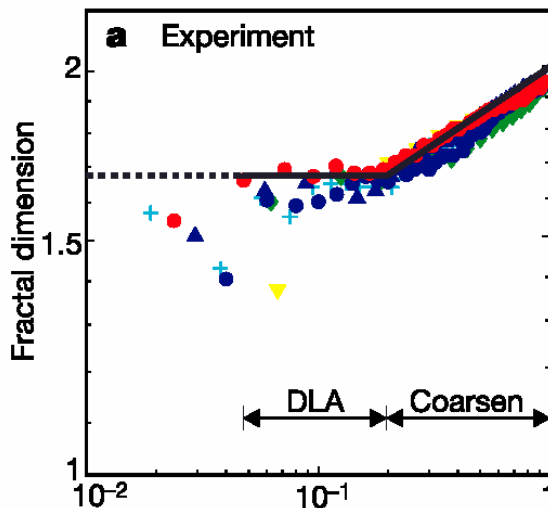
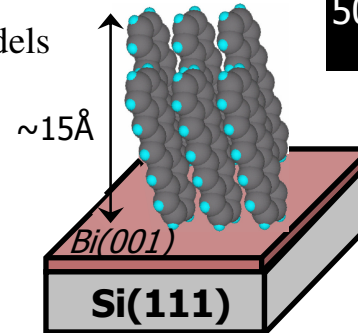
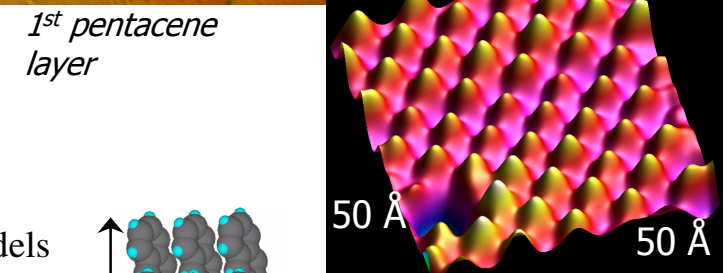
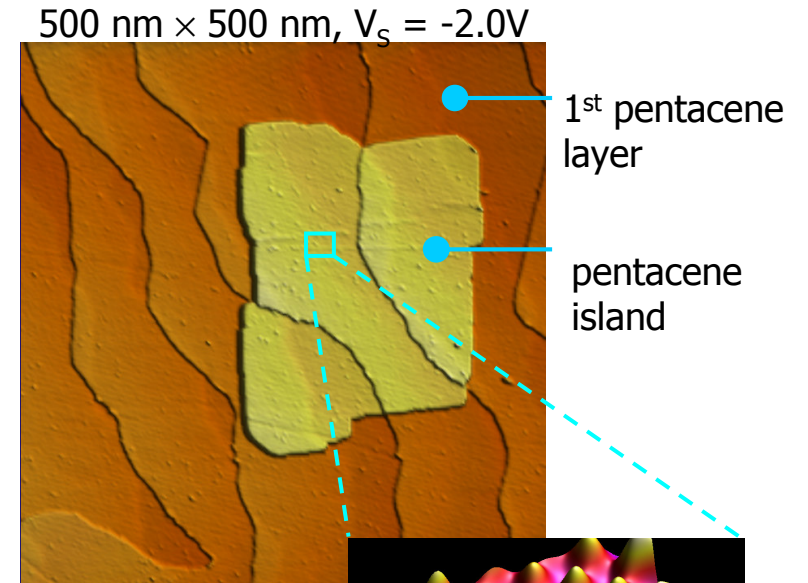
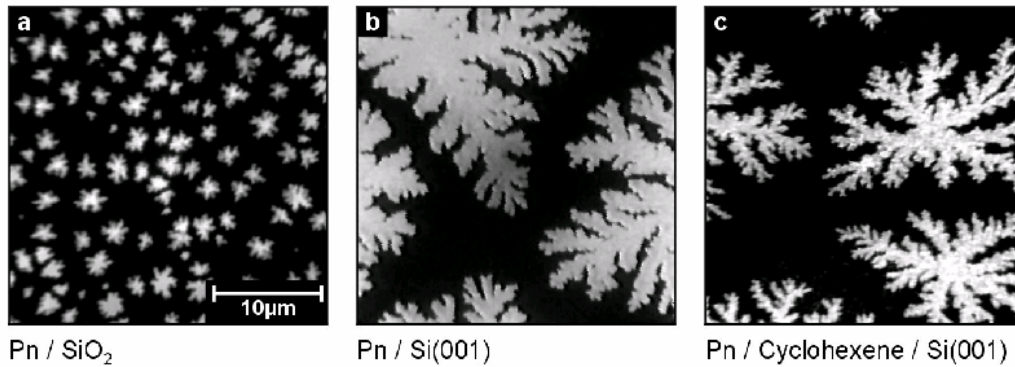


Mobilities up to 5 cm<sup>2</sup>/Volt.sec reported (a-Si: 1 cm<sup>2</sup>/Volt.sec)

Vacuum deposition



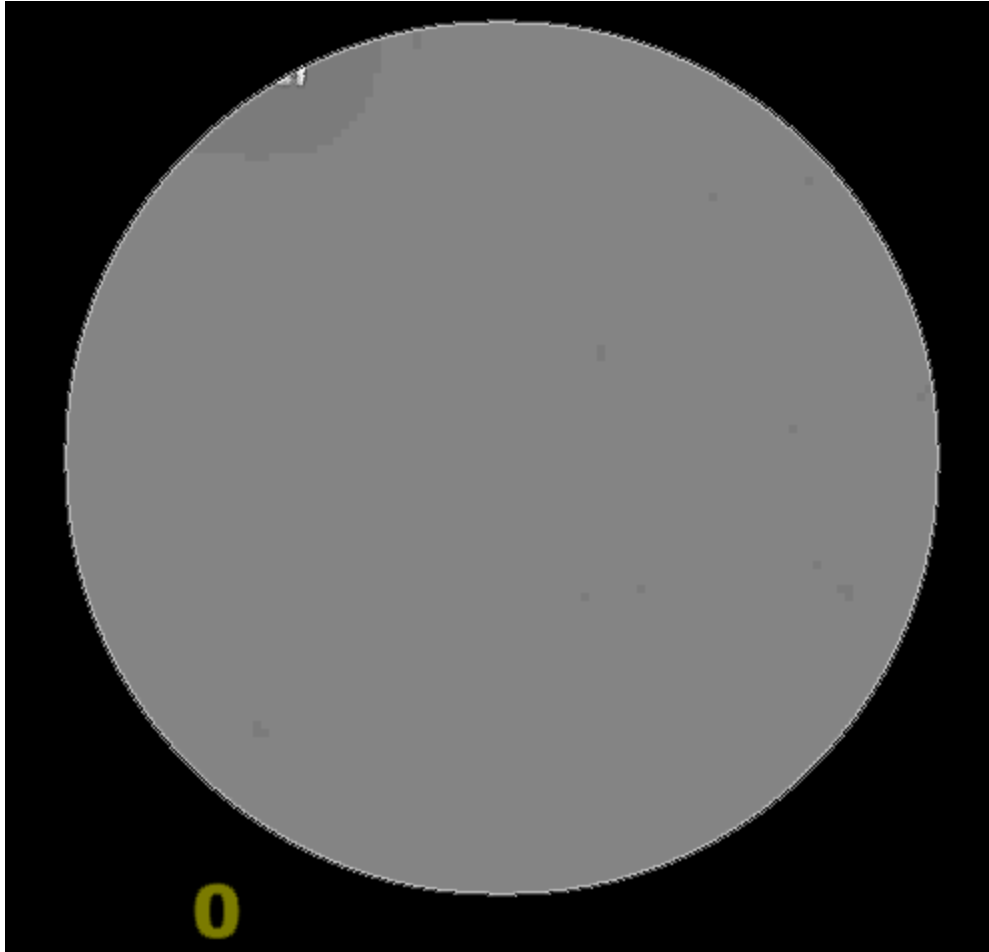
# Key Question: How do Molecules interact with Contacts?



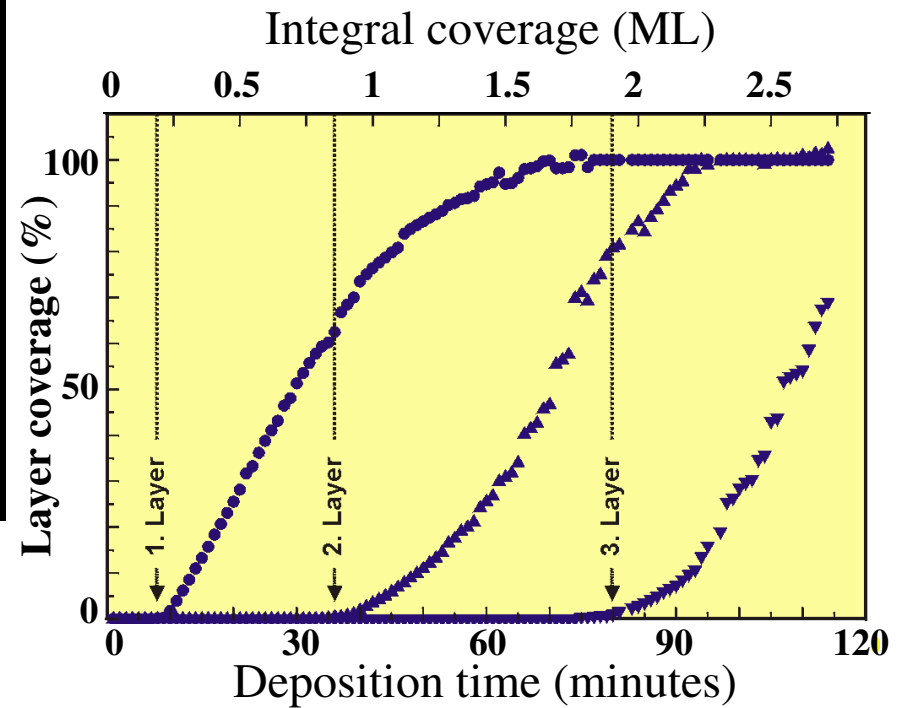
Pn stands up on non-metallic surfaces, such as oxides, semiconductors or semimetals, but lies down flat on metals.

Fractal growth and scaling is consistent with classical growth models developed for inorganic materials

# Growth on clean Si

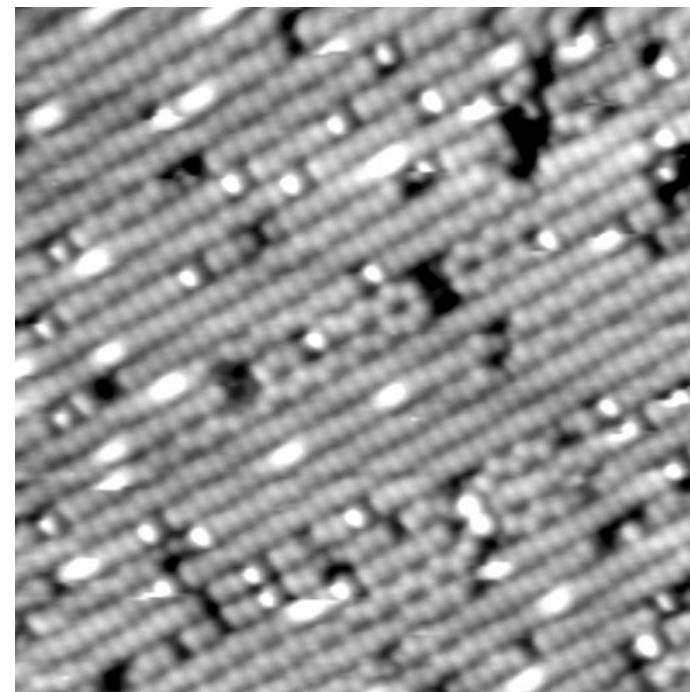
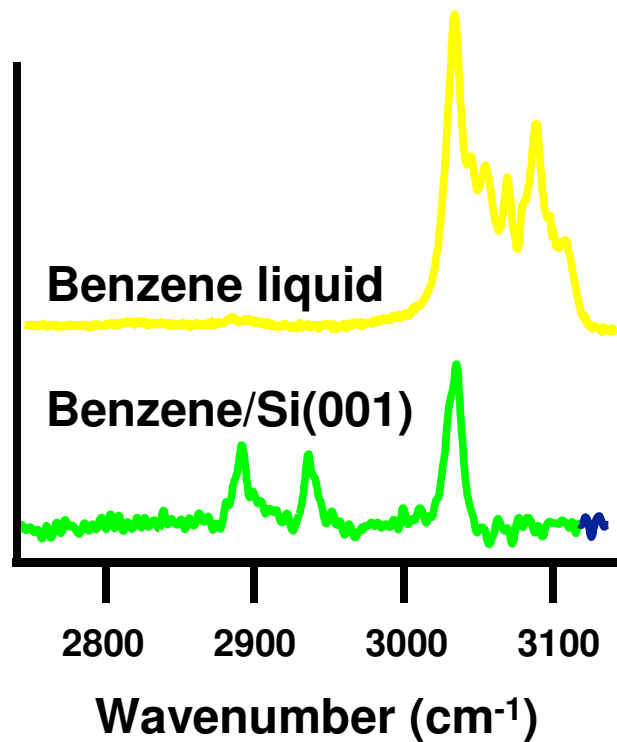
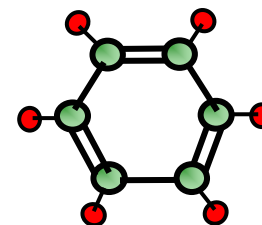


Field of view  $65 \mu\text{m}$   
Room temperature  
1 image/minute

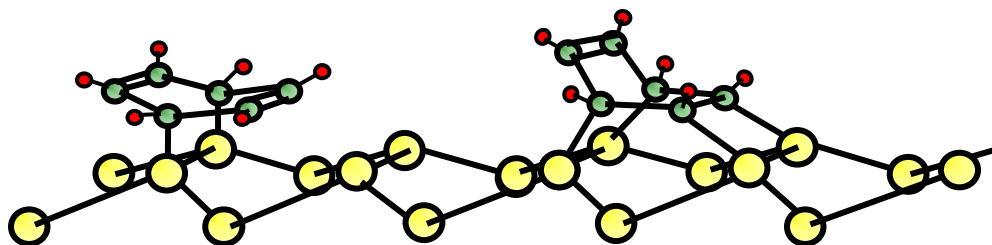




# Aromatic and Pi-conjugated systems:

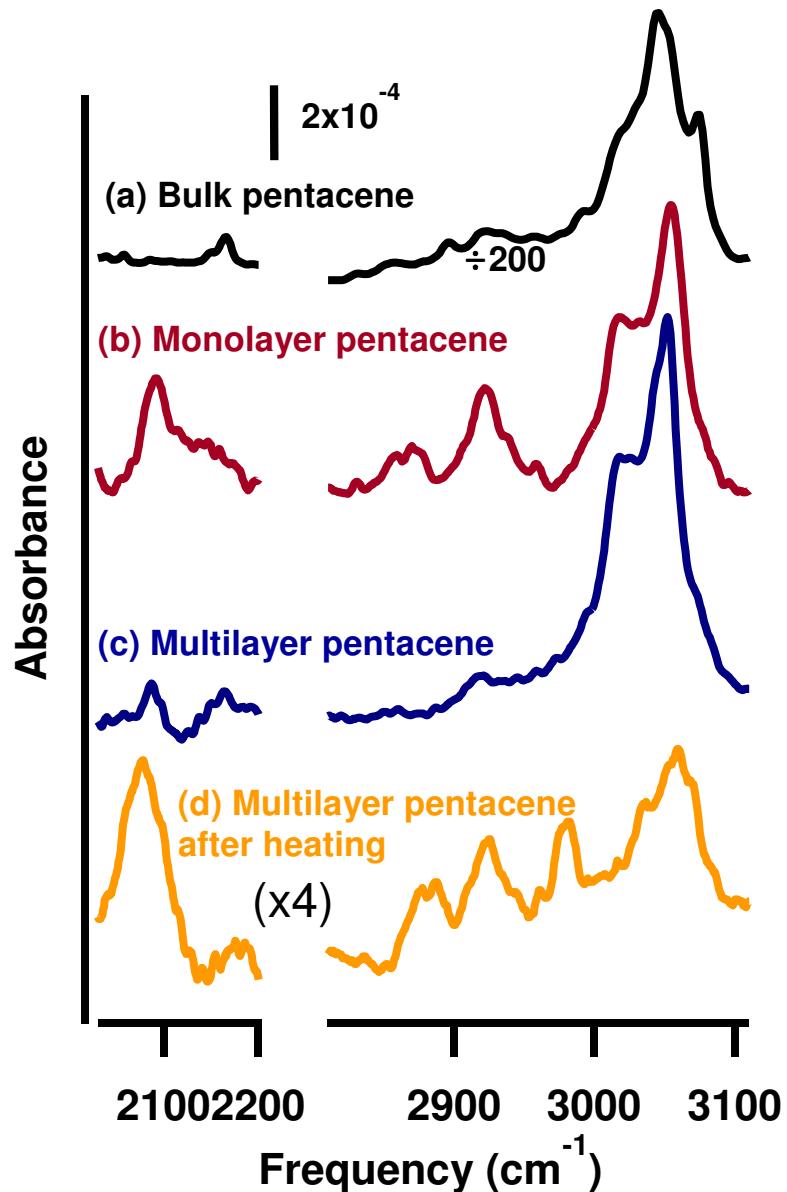


Multiple binding configurations



**Benzene/Si(001) also undergoes transition from  $sp^2$  to  $sp^3$  hybridization**  
***Reversibly adsorbs, desorbs***

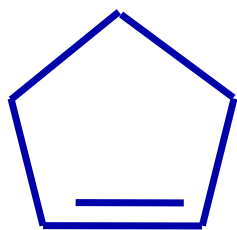
# Pentacene on Si(001): Infrared Spectra



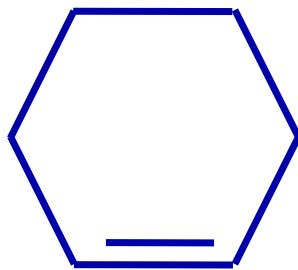
- Pentacene (bulk) shows no  $sp^3$  hybridized C-H stretching vibrations
- Monolayer coverage – peaks at 2091, 2870, 2922  $\text{cm}^{-1}$  indicate Si-H and  $sp^3$  hybridized C-H bonds, also thermally stable
- Multilayer coverage – small Si-H peak, peaks above 3000  $\text{cm}^{-1}$  are much larger than any below 3000  $\text{cm}^{-1}$
- Heating multilayer – broader Si-H peak, peaks almost identical to those of monolayer

Interfacial layer involves transition of some C atoms to  $sp^3$  hybridization and some dissociation. Layer appears to be thermally stable and *irreversibly* bound.

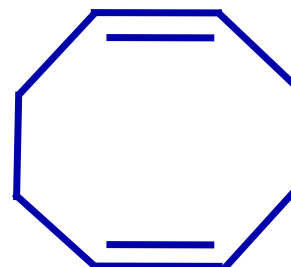
# Cyclo-addition reaction on Si



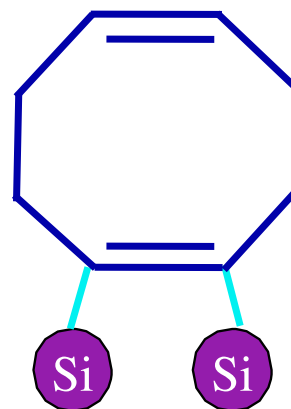
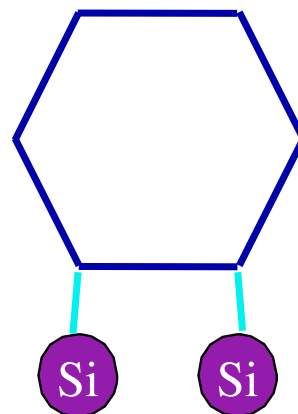
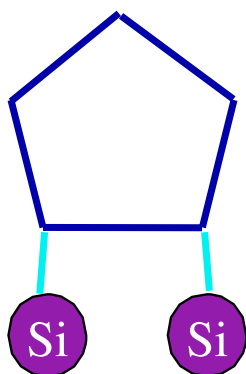
cyclopentene



cyclohexene



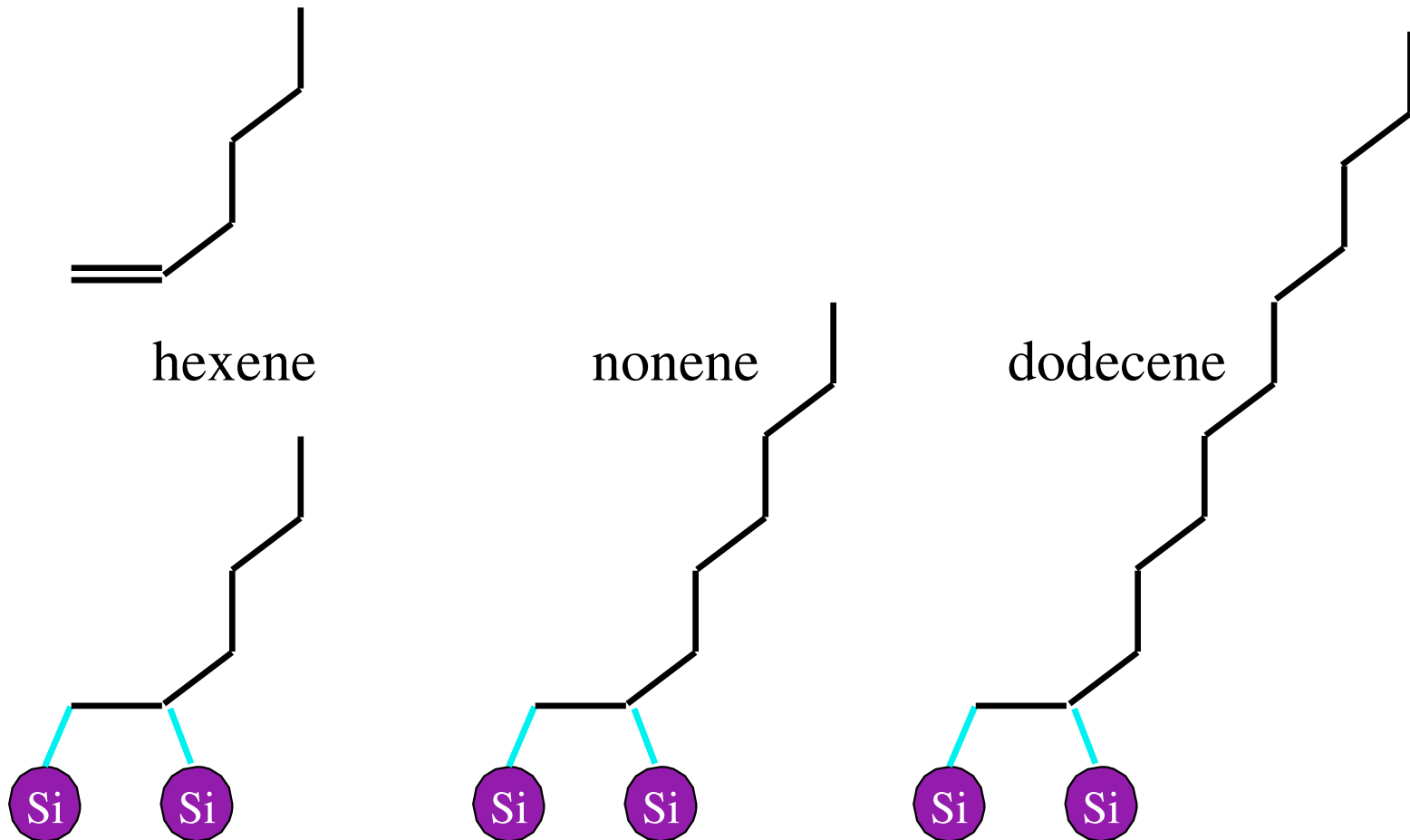
cyclo-octadiene



Organic surface termination renders Si surface inert, providing an ideal substrate for subsequent pentacene growth

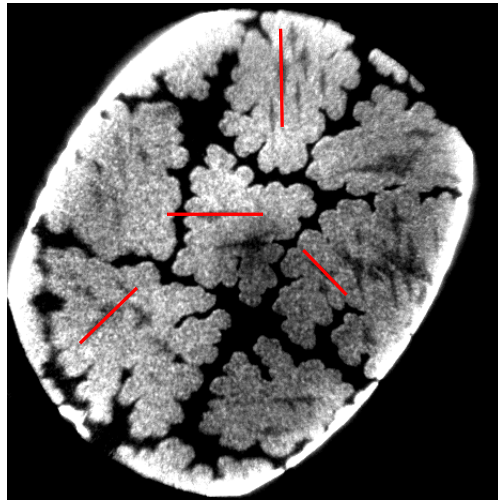


# From cyclical molecules to chains

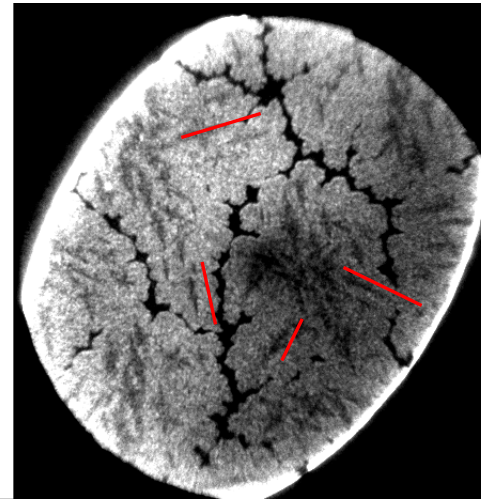


Chainlike molecules render Si surface inert, but affect diffusion and aggregation during pentacene growth

# Selection of azimuthal angles: Role of molecular species

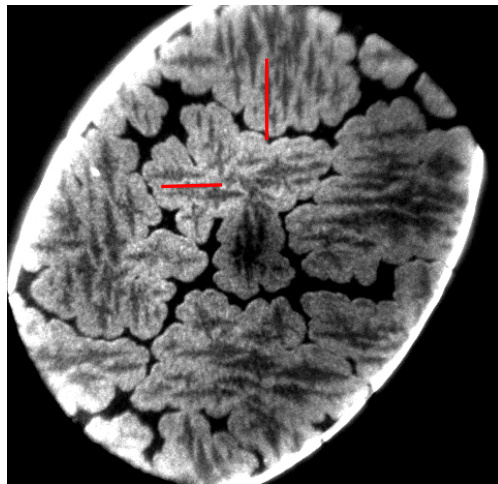


Cyclooctadiene

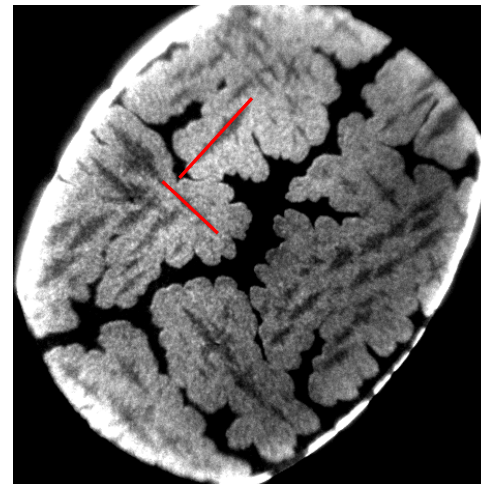


Hexene

Crystals at  
45 degrees  
increments



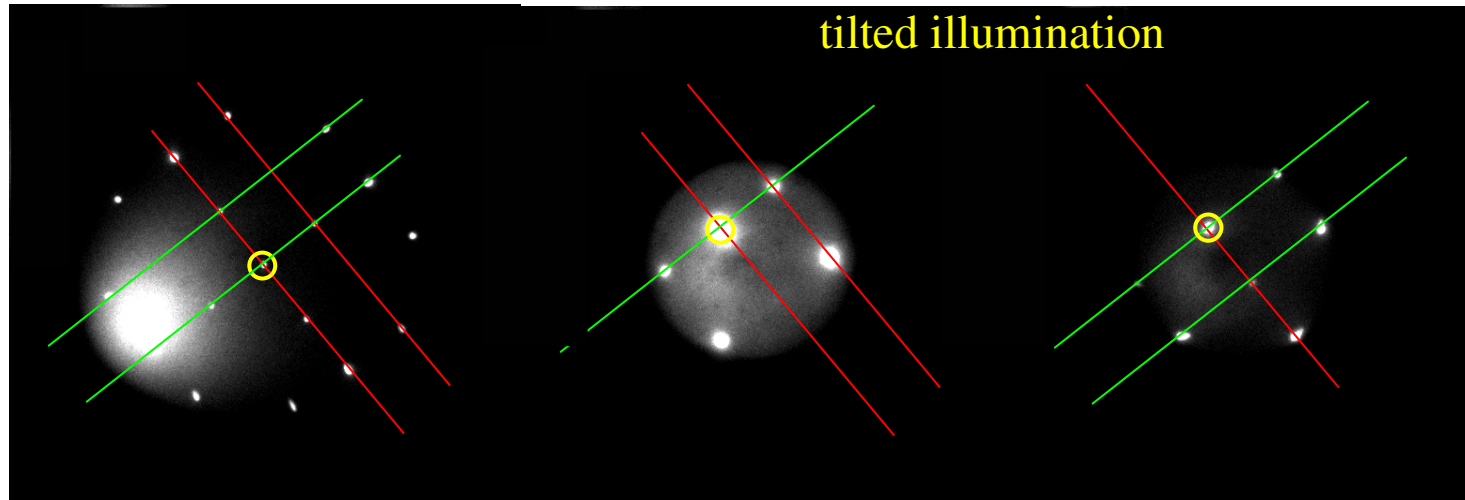
Dodecene on axis



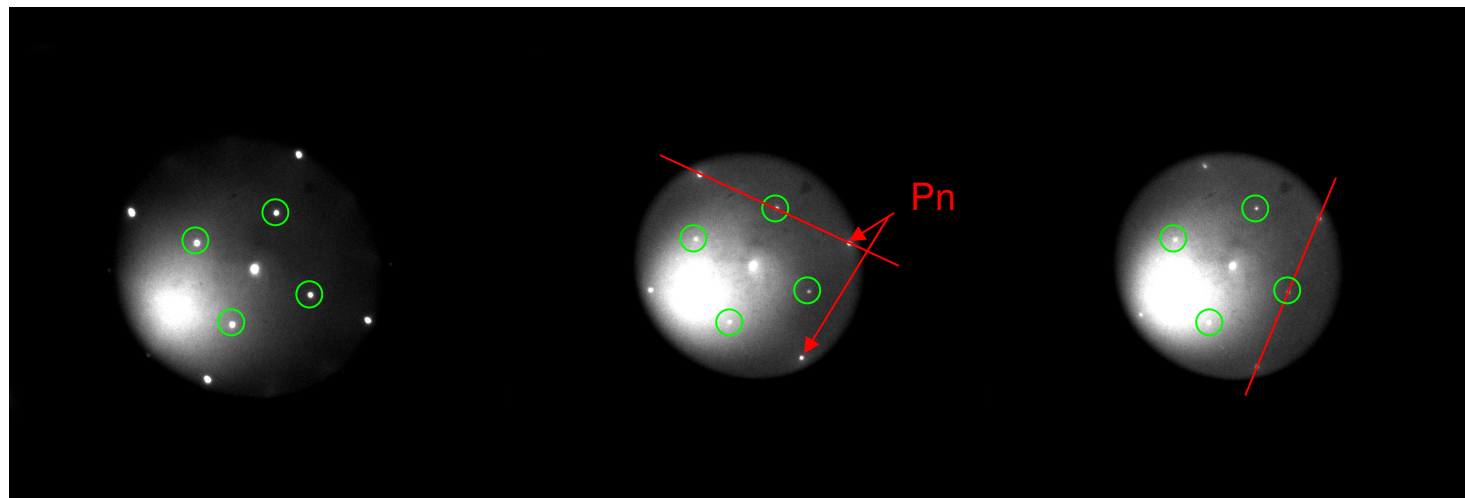
Dodecene off axis

Crystals at  
90 degrees  
increments

# Epitaxial growth of pentacene on Si: diffraction analysis



LEED 8/20 dodecene Si(001) on axis



Pentacene:

$a = 0.627$  nm

$b = 0.777$  nm

$\gamma = 84.7^\circ$

Si(001):

dimer-dimer: 0.384

row-row: 0.768

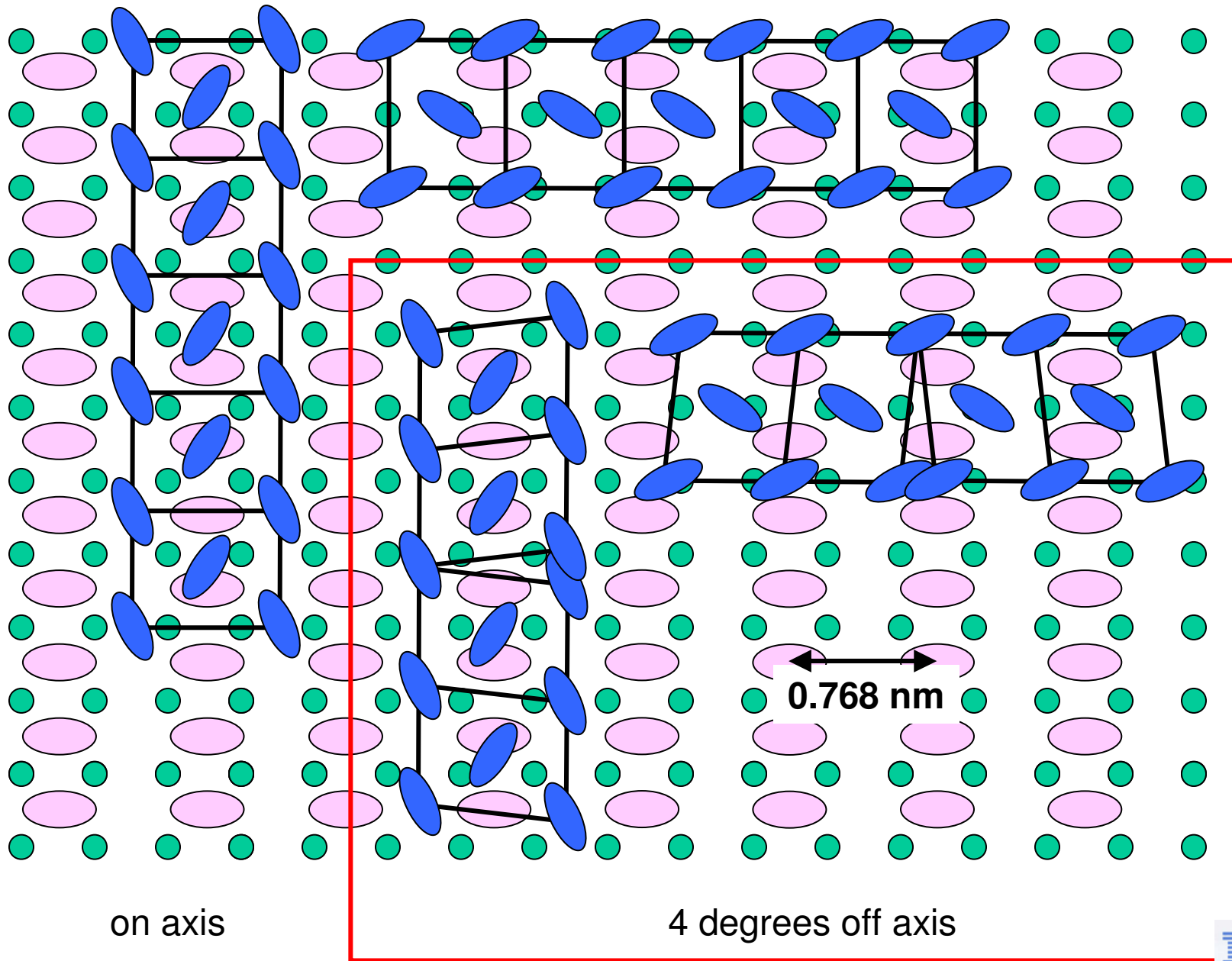
Mismatch  $\sim 1\%$

LEED 6/11 COD Si(001) on axis

Epitaxial growth, rectangular unit cell



# PENTACENE /Si(001) EPITAXY



# Pentacene MD on IBM BlueGene



classical  
diffusion



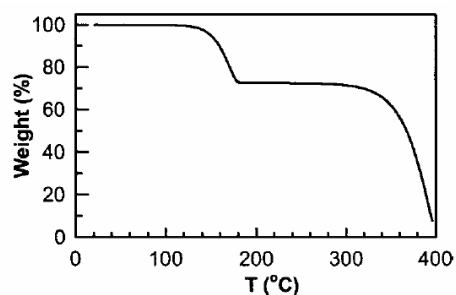
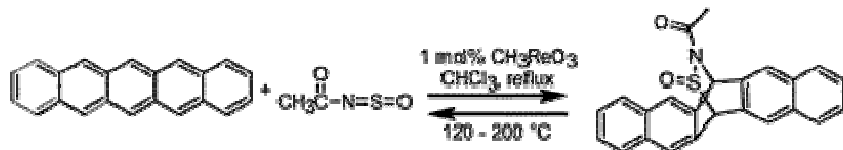
non - classical diffusion



pentacene diffusion on dodecene

# Soluble pentacene precursors

## Soluble pentacene for spincoated TFTs

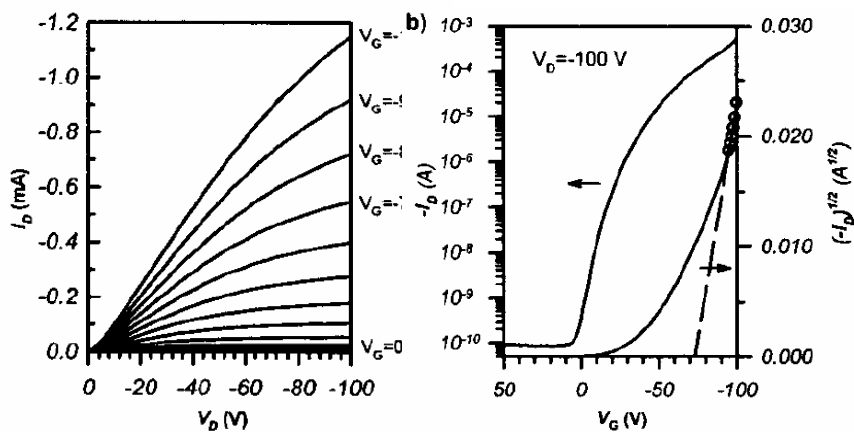


$$\mu_{\text{lin}} = 0.4$$

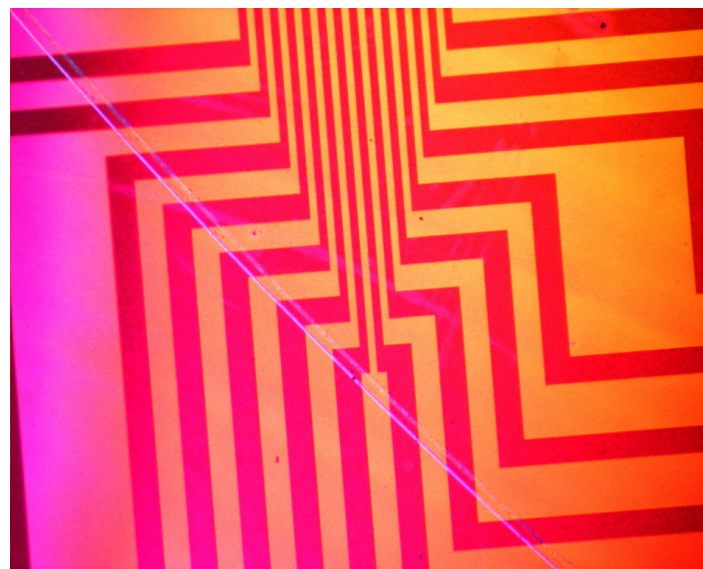
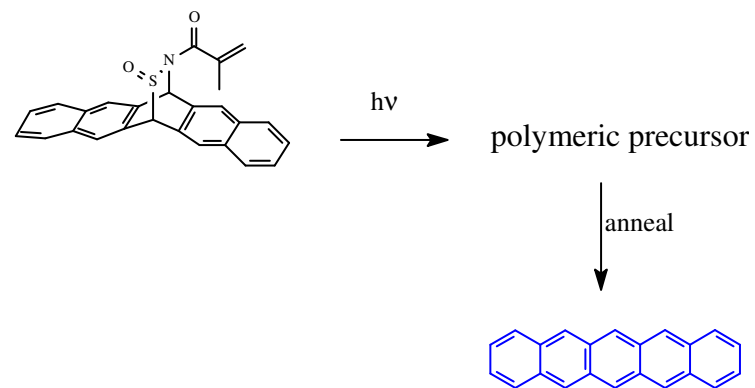
$$\mu_{\text{sat}} = 0.8$$

$$\text{on/off} > 10^6$$

Highest performance  
from solution processed  
organic TFT



## Photosensitive version for patterning





# Chalcogenides: a new look at some old materials

	$E_g$ (eV)	$\mu_n$ ( $\text{cm}^2/\text{V-sec}$ )	$\mu_p$ ( $\text{cm}^2/\text{V-sec}$ )
SnS <sub>2</sub>	2.6	18	
SnSe <sub>2</sub>	1.6	27	
ZnSe	2.7	600	
ZnTe	2.25		100
CdS	2.42	250	
CdSe	1.73	650	

## Semiconductor mobilities @ RT ( $\text{cm}^2/\text{V.s}$ )

InSb	77,000
CdSe	650
c-Si	1,500 – 100
a-Si	1
CNT	100,000
Polymer	$10^{-5} - 10^{-2}$
Pentacene	0.1 – 5
Chalcogenides (spinc.)	1 -20

Organic-derivatized CdSe nanocrystals -- n-type  $1.0 \text{ cm}^2/\text{V-s}$

[Ridley et. al., Science 286, 746 (1999)]

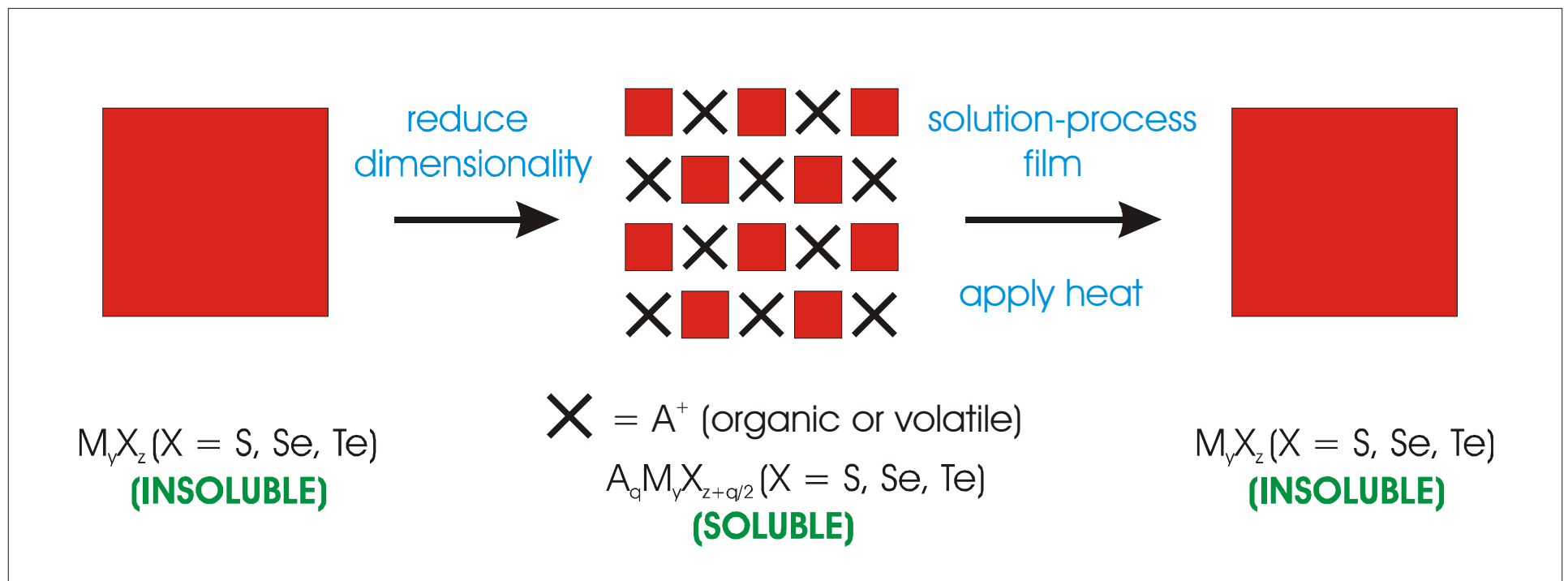
Chemical Bath Technique CdSe -- n-type  $15 \text{ cm}^2/\text{V-s}$

[Gan et. al., IEEE Trans. Electr. Devices 49, 15 (2002)]

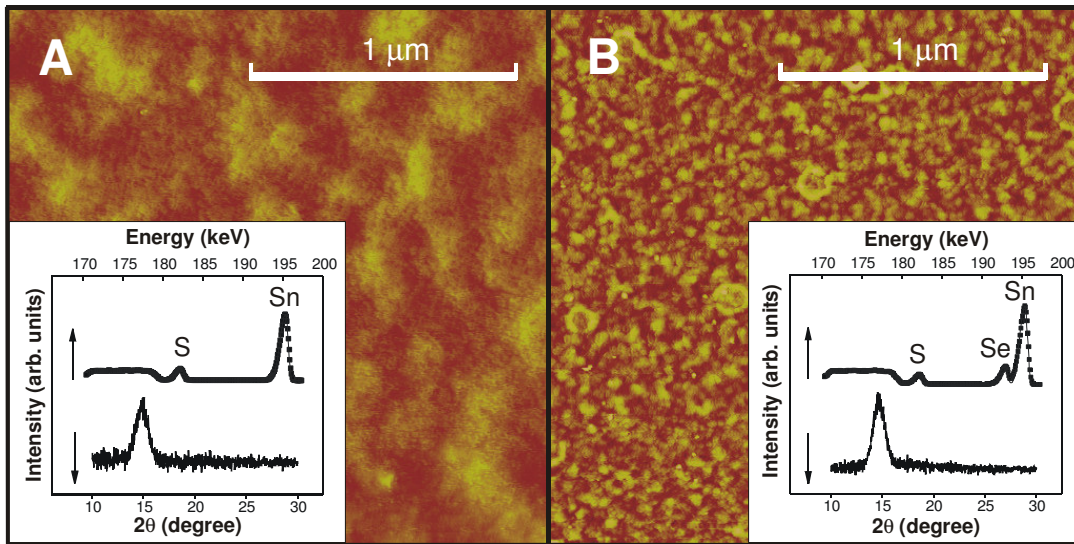
Assembled Nanorod / Nanoribbon -- n-, p-type  $<300 \text{ cm}^2/\text{V-s}$

[Duan et. al., Nature 425, 274 (2003)]

# Solution Processable Chalcogenides



# Chalcogenides – a new low-T spin-on semiconductor with high mobility

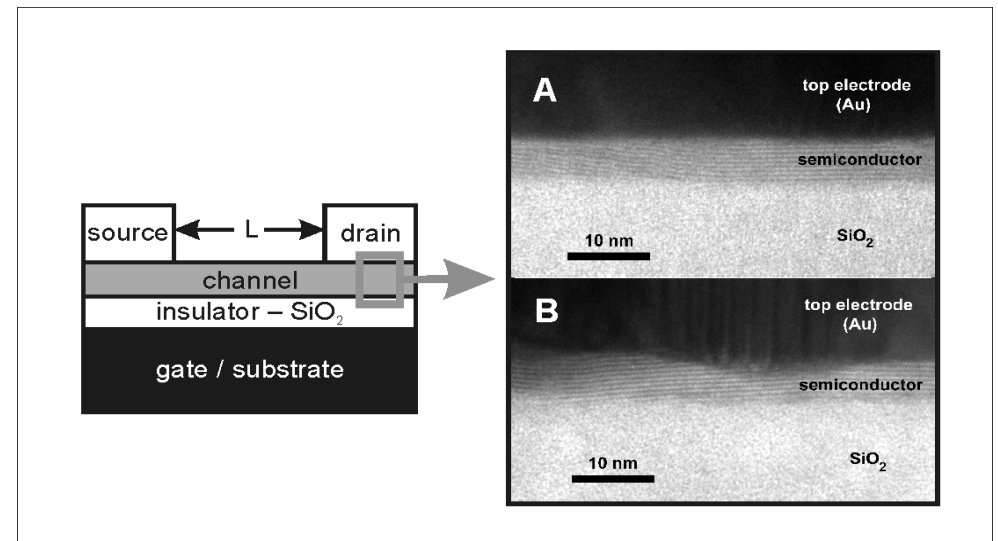


Developed new chemistries to spin on thin chalcogenide films with low processing temperatures < 300 C

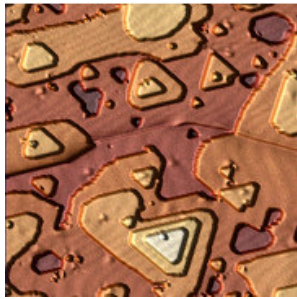
RMS Roughness: 6 Å (A) and 14 Å (B)  
 Film thickness: only 7- 9 unit cells thick!  
 Composition:  $\text{SnS}_{1.8}$  and  $\text{SnS}_{1.4}\text{Se}_{0.5}$

$\mu_{\text{sat}} = 12.0 \text{ cm}^2/\text{V-s}$   
 $\mu_{\text{lin}} = 2.4 \text{ cm}^2/\text{V-s}$   
 $I_{\text{on}}/I_{\text{off}} > 10^6$   
 $L = 14 \mu\text{m}; W = 250 \mu\text{m}$

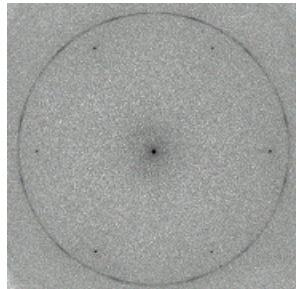
Highest spin-coated channel mobility by ~ 10X



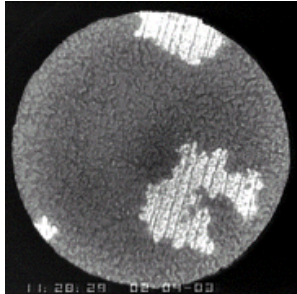




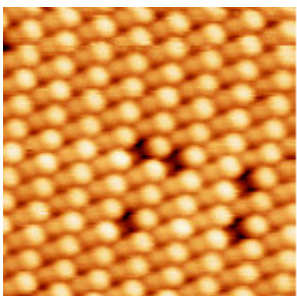
Nanoscience may give rise to future revolutionary technologies relevant to Information Technology: Nanotube or molecular logic; Novel, dense crosspoint memories; New high performance spin-coatable semiconductors; etcetera.



Also, Si technology itself is a rapidly changing revolutionary technology that provides a 'ready' platform for nanotechnology integration:



Materials; Processes; Devices; Lithography; etcetera.



Development of new technologies to replace an existing technology takes decades, not months – but we can utilize nanotechnology on shorter timescales by insertion in existing technologies, and in niche application.

